

Notice of References CitedApplication No.
08/660,016

Applicant(s)

Jack D. Pippin

Examiner

Vuthe Siek

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2304**Page 1 of 1****U.S. PATENT DOCUMENTS**

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
A	4,807,144	Feb 21, 89	Joehlin et al.	364	473
B					
C					
D					
E					
F					
G					
H					
I					
J					
K					
L					
M					

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
N						
O						
P						
Q						
R						
S						
T						

NON-PATENT DOCUMENTS

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
U	Allen et al. "CMOS ANALOG CIRCUIT DESIGN," BY SAUNDERS COLLEGE PUBLISHING, A DIVISION OF HOLT, RINEHART AND WINSTON, PP. 529-543.	1987
V		
W		
X		